

**• Product Summary**

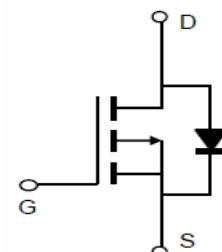
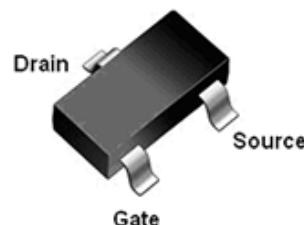
Part #	V <sub>DS</sub>	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =4.5V)	R <sub>DS(on).typ</sub> (@V <sub>GS</sub> =2.5V)	I <sub>D</sub>
EFM2305	-20V	30mΩ	45mΩ	-4.2A

**• Features**

- Low R<sub>DS(on)</sub> @ V<sub>GS</sub>=-4.5V
- -2.5V Logic Level Control
- P Channel SOT23 Package
- Pb-Free, RoHS Compliant

**• Application**

- Charging switch for portable devices
- Small brushless DC motor drive
- Load Switch for Portable Devices
- DC-to-DC converters
- Power Management Functions


**P-Channel MOSFET**

**• Ordering Information:**

Part NO.	EFM2305
Marking	S5
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

**• Absolute Maximum Ratings (T<sub>C</sub>=25°C)**

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-20	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Drain Current-Continuous	I <sub>D</sub>	-4.2	A
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	-16.8	A
Maximum Power Dissipation	P <sub>D</sub>	1.25	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 To 150	°C

**• Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	R <sub>θJA</sub>	100	°C/W
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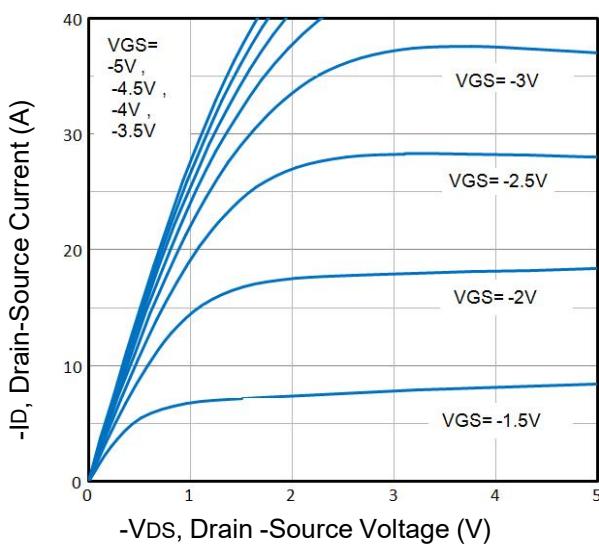
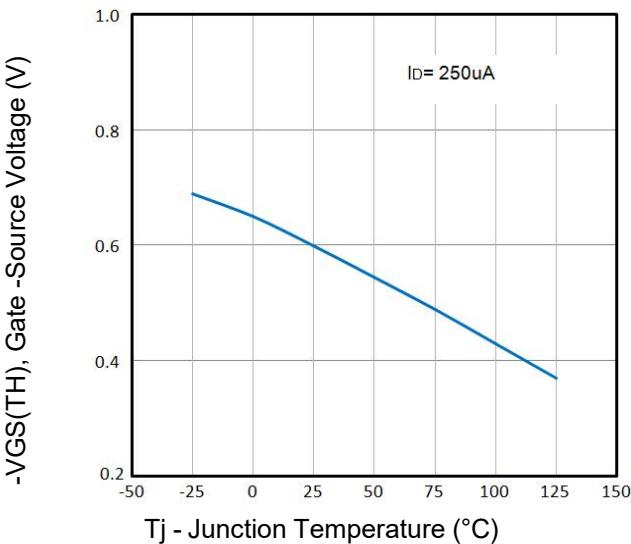
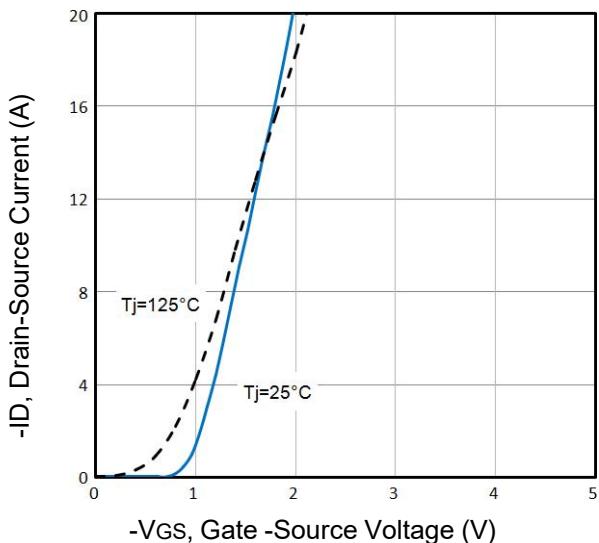
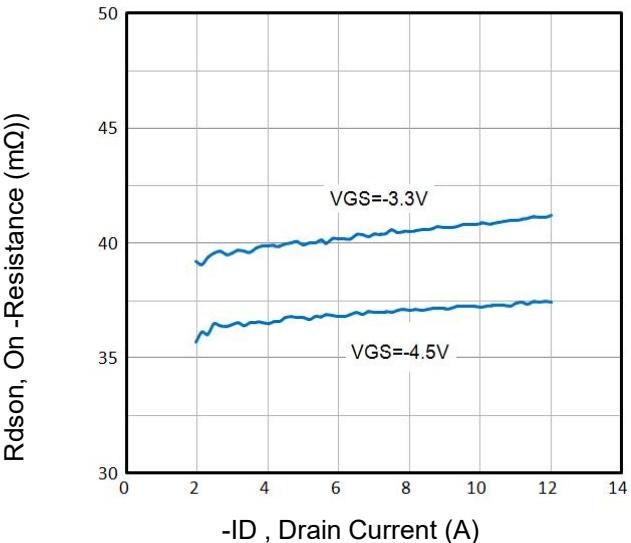
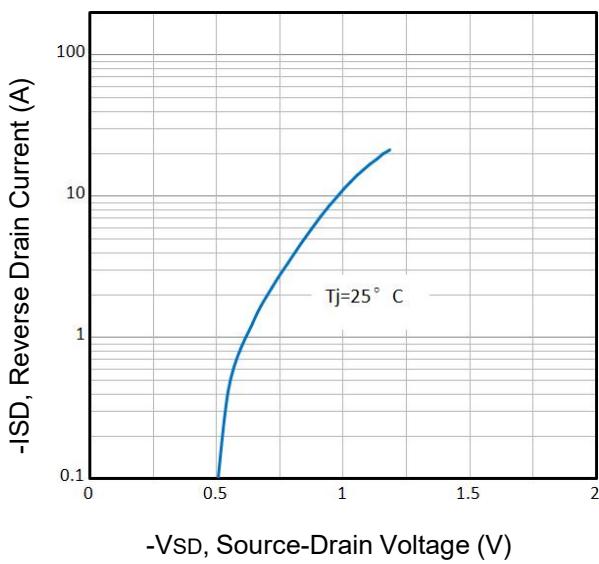
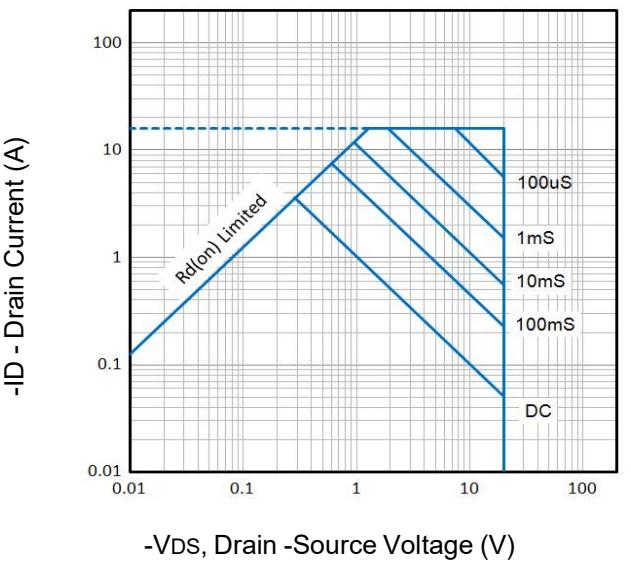
**• Static Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise stated)**

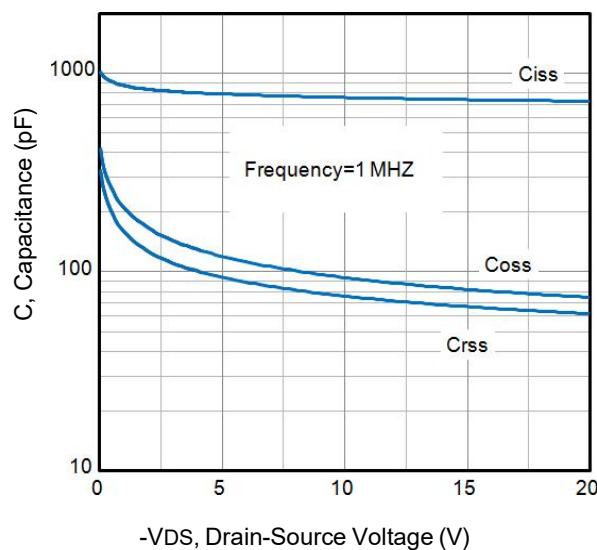
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V} I_{\text{D}}=250\mu\text{A}$	-20	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-20\text{V} V_{\text{GS}}=0\text{V}$	--	--	-1	nA
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 10\text{V} V_{\text{DS}}=0\text{V}$	--	--	$\pm 100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}} I_{\text{D}}=-250\mu\text{A}$	-0.4	-0.7	-1.0	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=-4.5\text{V} I_{\text{D}}=-4\text{A}$	--	30	38	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V} I_{\text{D}}=-3\text{A}$	--	45	60	$\text{m}\Omega$
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-10\text{V} V_{\text{GS}}=0\text{V}$ $F=1.0\text{MHz}$	--	760	--	PF
Output Capacitance	$C_{\text{oss}}$		--	94	--	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	76	--	PF
Gate Resistance	$R_g$	$F=1.0\text{MHz}$	--	16	--	$\Omega$
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=-10\text{V} I_{\text{D}}=-2\text{A}$ $V_{\text{GS}}=-4.5\text{V} R_{\text{G}}=3.3\Omega$	--	5.5	--	nS
Turn-on Rise Time	$t_r$		--	3.9	--	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		--	11.3	--	nS
Turn-Off Fall Time	$t_f$		--	36	--	nS
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-10\text{V} I_{\text{D}}=-3\text{A}$ $V_{\text{GS}}=-4.5\text{V}$	--	7.6	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	0.9	--	nC
Gate-Drain Charge	$Q_{\text{gd}}$		--	1.8	--	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V} I_{\text{S}}=-4\text{A}$	--	-0.87	-1.2	V
Diode Forward Current <sup>(Note 2)</sup>	$I_{\text{S}}$		--	--	-2	A

**Notes:**

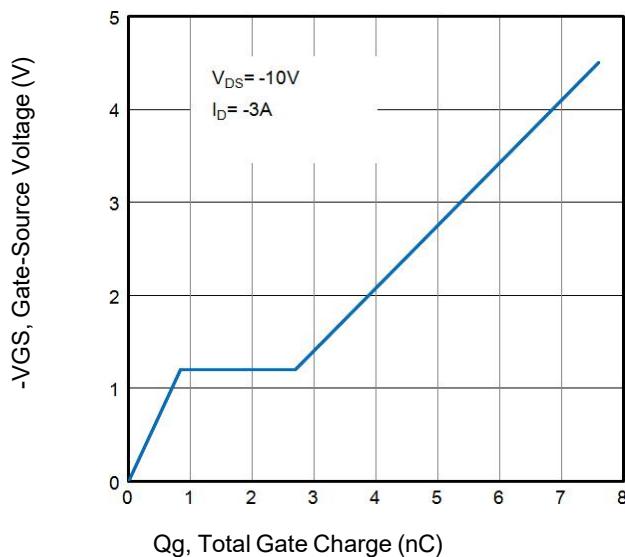
① Pulse width limited by maximum allowable junction temperature

② Pulse test ; Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .

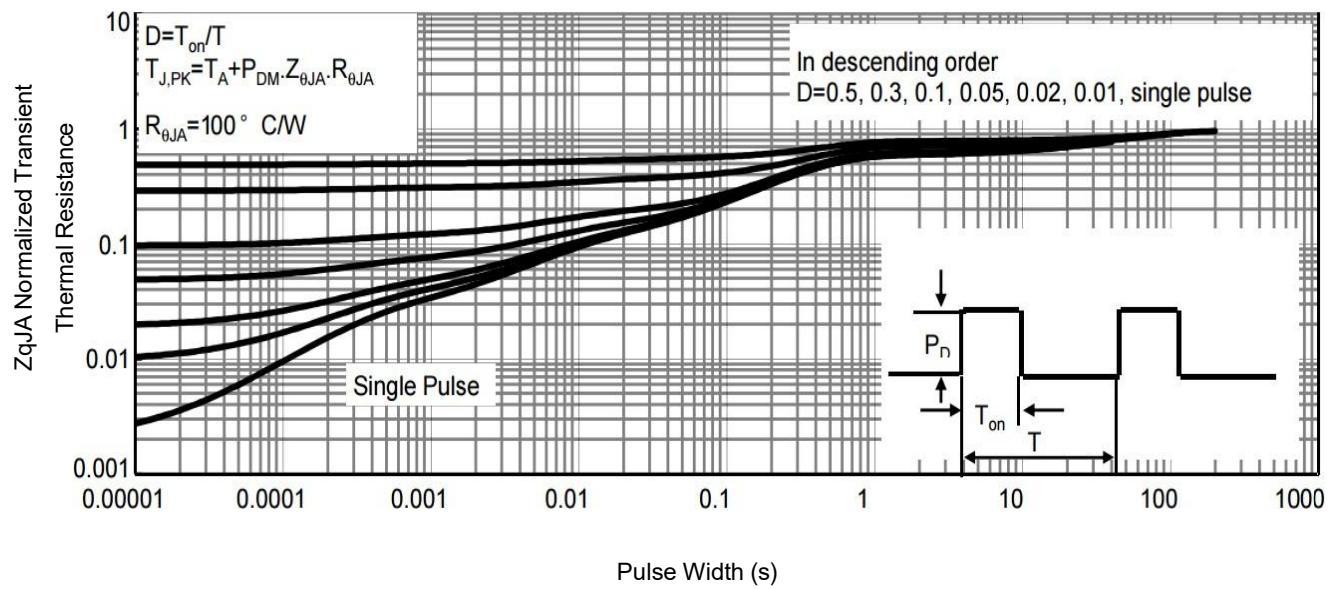
**• Typical Characteristics**

**Fig1.** Typical Output Characteristics

**Fig2.** Normalized Threshold Voltage Vs. Temperature

**Fig3.** Typical Transfer Characteristics

**Fig4.** On-Resistance vs. Drain Current and Gate Voltage

**Fig5.** Typical Source-Drain Diode Forward Voltage

**Fig6.** Maximum Safe Operating Area



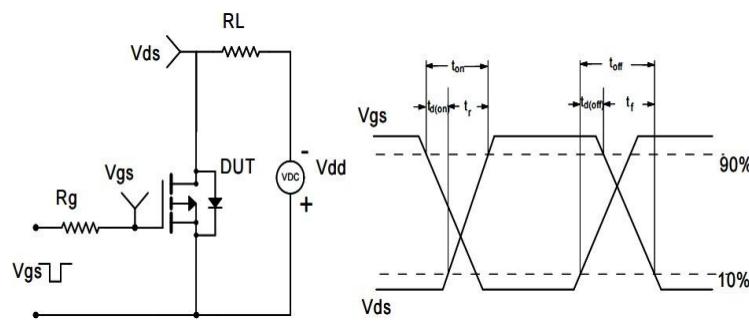
**Fig7.** Typical Capacitance Vs. Drain-Source Voltage



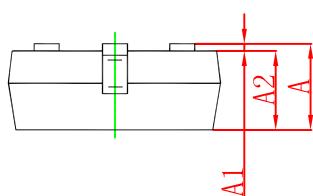
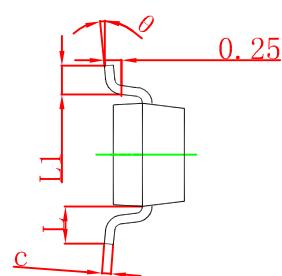
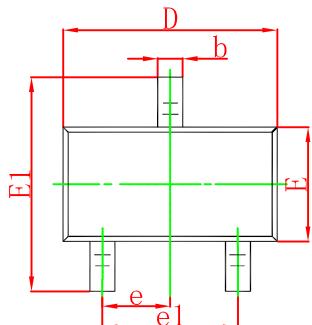
**Fig8.** Typical Gate Charge Vs. Gate-Source Voltage



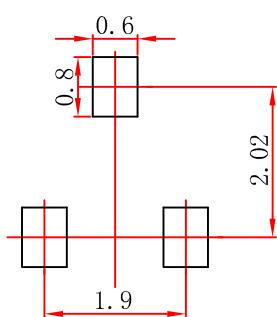
**Fig9.** Normalized Maximum Transient Thermal Impedance



**Fig10.** Switching Time Test Circuit and waveforms

**SOT-23 Package Outline Dimensions**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°


**Note:**

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.