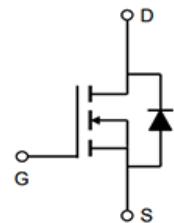


• Product Summary

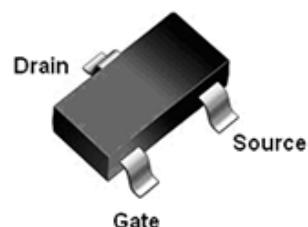
Part #	V _{DS}	R _{DS(on).typ} (@V _{GS} =4.5V)	R _{DS(on).typ} (@V _{GS} =2.5V)	I _D
EFM2302	20V	28mΩ	35mΩ	3.6A


• Features

- Low R_{DS(on)} @ V_{GS}=-4.5V
- -2.5V Logic Level Control
- N Channel SOT23 Package
- Pb-Free, RoHS Compliant

N-Channel MOSFET
• Application

- Load Switch
- DC/DC Converter
- Switching Circuits
- LED Driver


• Ordering Information:

Part NO.	EFM2302
Marking	A2SHB
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

HF
• Absolute Maximum Ratings (T_C=25°C)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±12	V
Drain Current-Continuous	I _D	3.6	A
Drain Current-Pulsed (Note 1)	I _{DM}	18	A
Maximum Power Dissipation	P _D	1.2	W
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 To 150	°C

• Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{θJA}	100	°C/W
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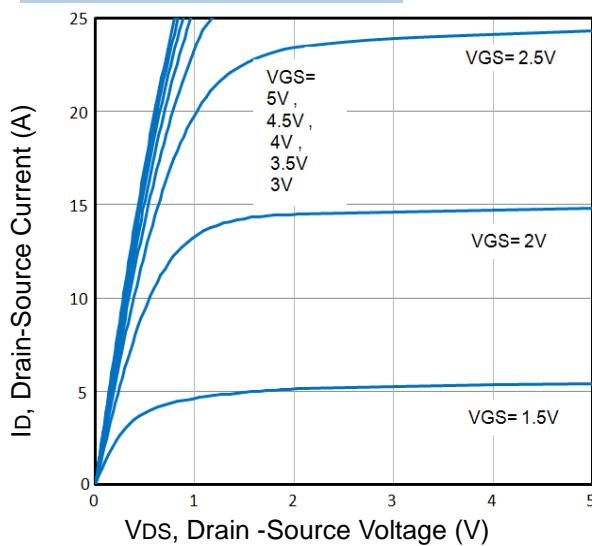
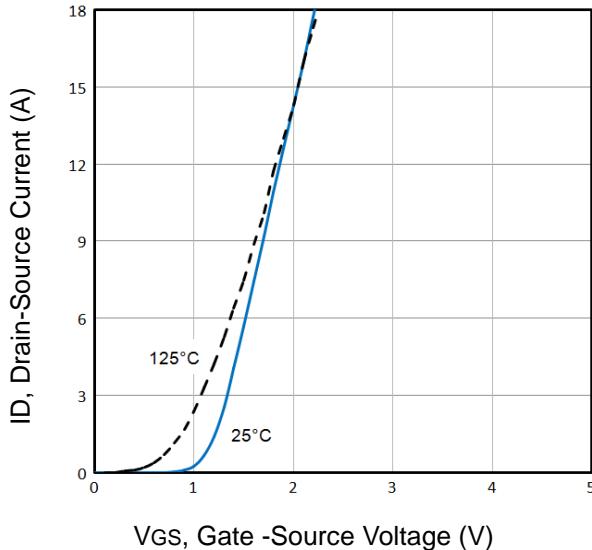
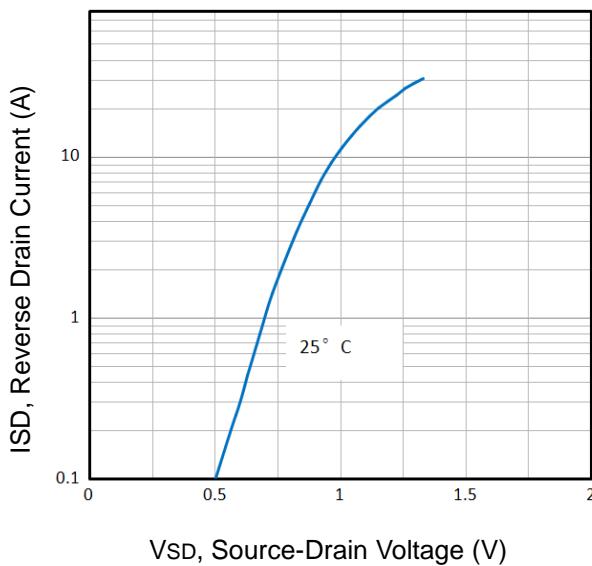
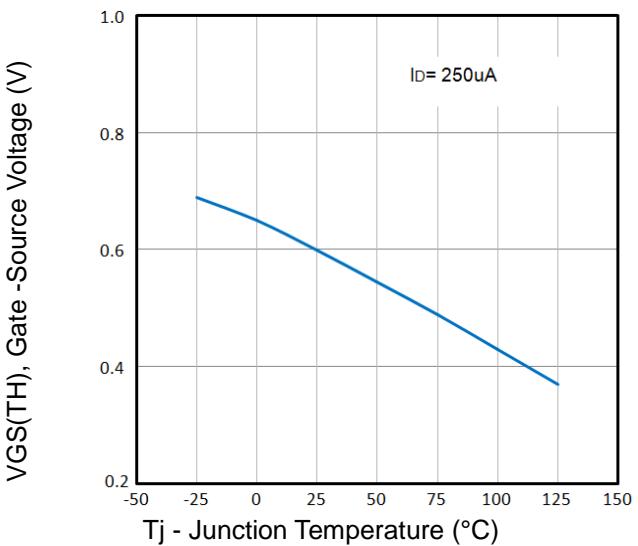
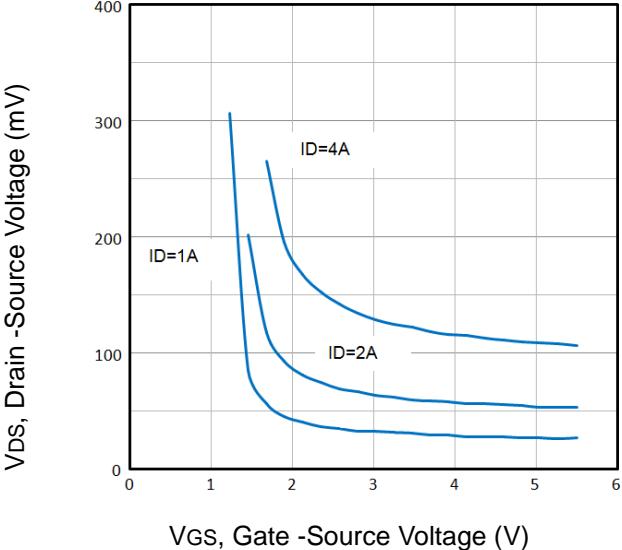
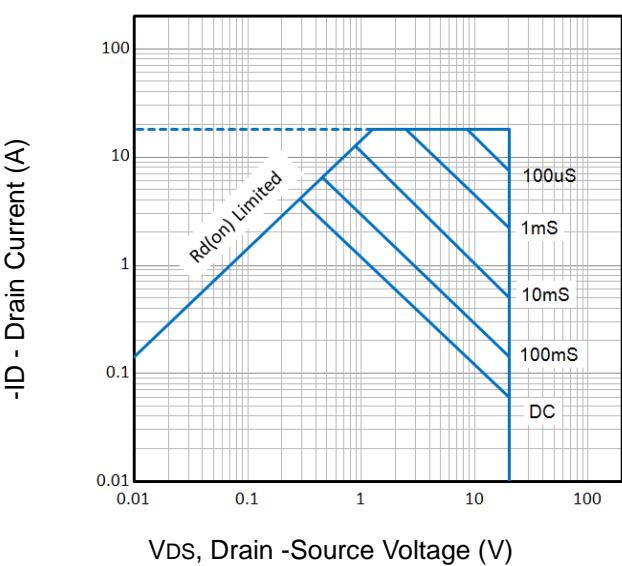
• Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V} I_{\text{D}}=250\mu\text{A}$	20	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=20\text{V} V_{\text{GS}}=0\text{V}$	--	--	1	nA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 12\text{V} V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}} I_{\text{D}}=250\mu\text{A}$	0.4	0.6	1.0	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=4.5\text{V} I_{\text{D}}=3.6\text{A}$	--	28	35	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V} I_{\text{D}}=3\text{A}$	--	35	45	$\text{m}\Omega$
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C_{iss}	$V_{\text{DS}}=10\text{V} V_{\text{GS}}=0\text{V}$ $F=1.0\text{MHz}$	--	280	--	PF
Output Capacitance	C_{oss}		--	46	--	PF
Reverse Transfer Capacitance	C_{rss}		--	42	--	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=10\text{V} I_{\text{D}}=4\text{A}$ $V_{\text{GS}}=-4.5\text{V} R_{\text{G}}=3.3\Omega$	--	11	--	nS
Turn-on Rise Time	t_{r}		--	35	--	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		--	25	--	nS
Turn-Off Fall Time	t_{f}		--	32	--	nS
Total Gate Charge	Q_{g}	$V_{\text{DS}}=10\text{V} I_{\text{D}}=3\text{A}$ $V_{\text{GS}}=-4.5\text{V}$	--	4.7	--	nC
Gate-Source Charge	Q_{gs}		--	0.6	--	nC
Gate-Drain Charge	Q_{gd}		--	1.7	--	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V_{SD}	$V_{\text{GS}}=0\text{V} I_{\text{S}}=-2\text{A}$	--	0.74	1.2	V
Diode Forward Current ^(Note 2)	I_{S}		--	--	1.8	A

Notes:

① Pulse width limited by maximum allowable junction temperature

② Pulse test ; Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

• Typical Characteristics

Fig1. Typical Output Characteristics

Fig3. Typical Transfer Characteristics

Fig5. Typical Source-Drain Diode Forward Voltage

Fig2. Normalized Threshold Voltage Vs. Temperature

Fig4. Drain -Source Voltage vs Gate -Source Voltage

Fig6. Maximum Safe Operating Area

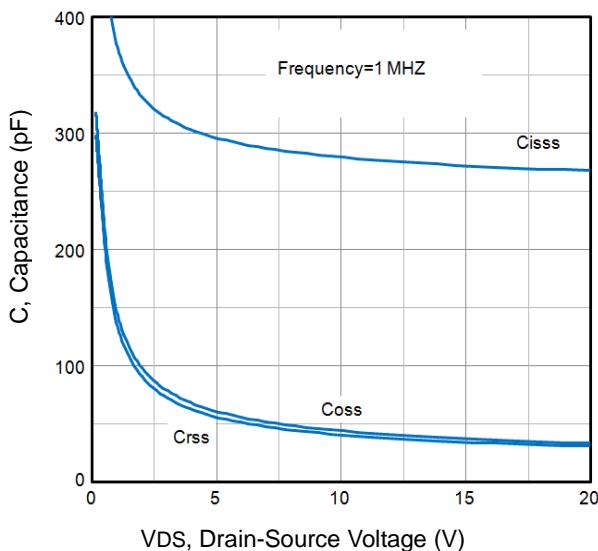


Fig7. Typical Capacitance Vs. Drain-Source Voltage

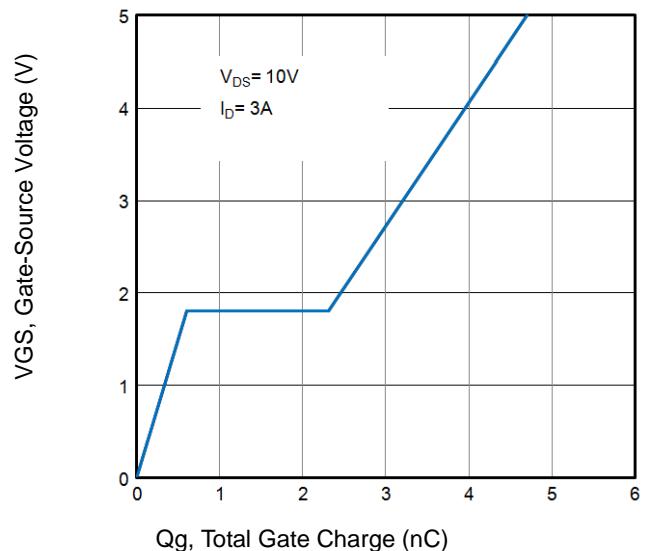


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

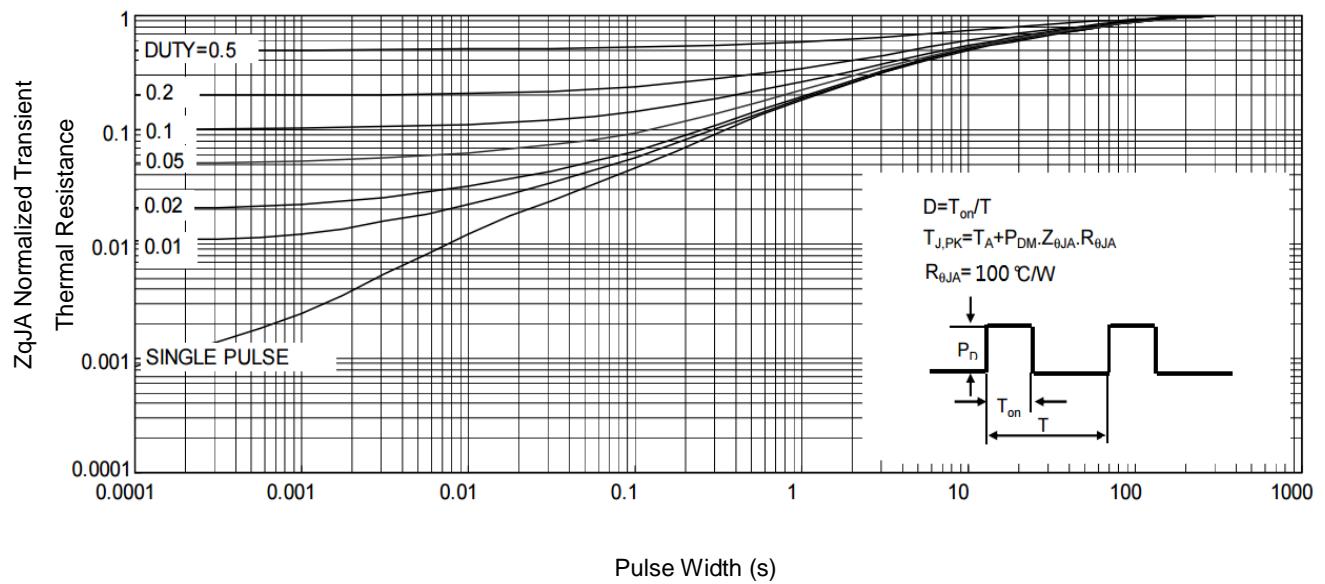


Fig9. Normalized Maximum Transient Thermal Impedance

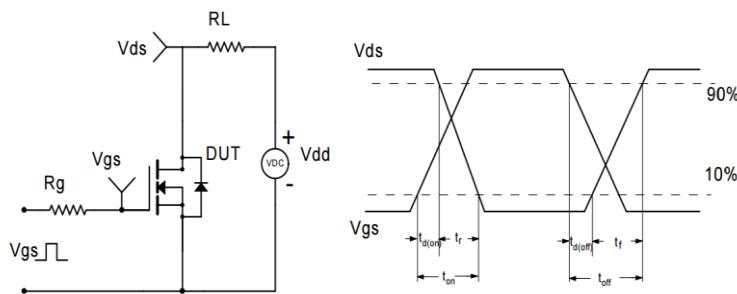
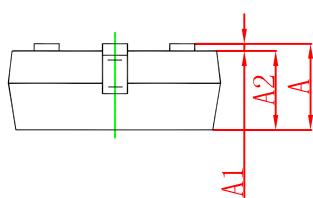
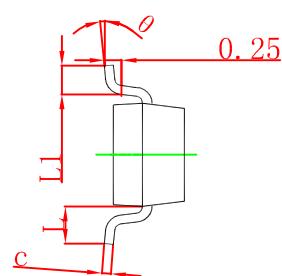
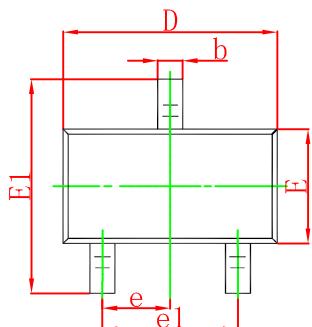
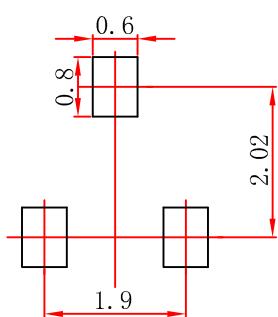


Fig10. Switching Time Test Circuit and waveforms

SOT-23 Package Outline Dimensions


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°


Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.